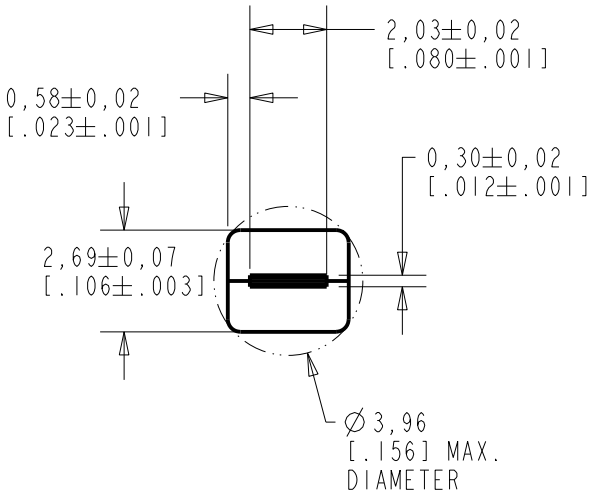
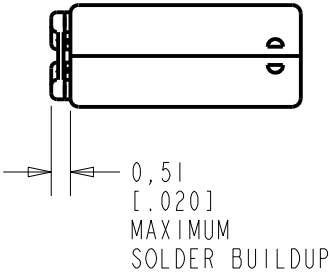
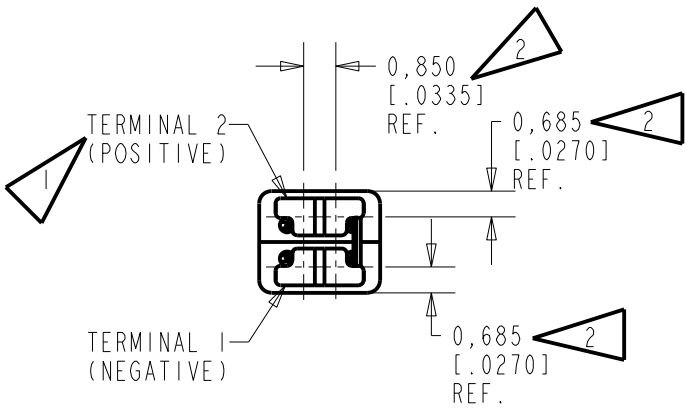
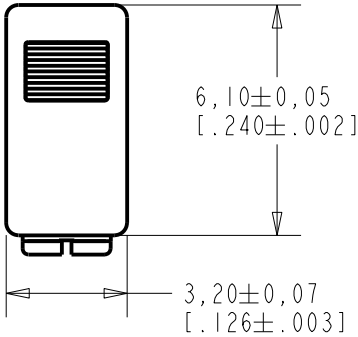


GD30-31719-000
SHT 1.1

NOTES:

- 1 A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO TERMINAL 1, CAUSES AN INCREASE IN PRESSURE AT THE SOUND OUTLET.
- 2 DIMENSION TO APPROXIMATE CENTER OF TERMINAL PAD.



SCALE 2:1
 0.19 GRAMS
 DIMENSIONS IN MILLIMETERS [INCHES]

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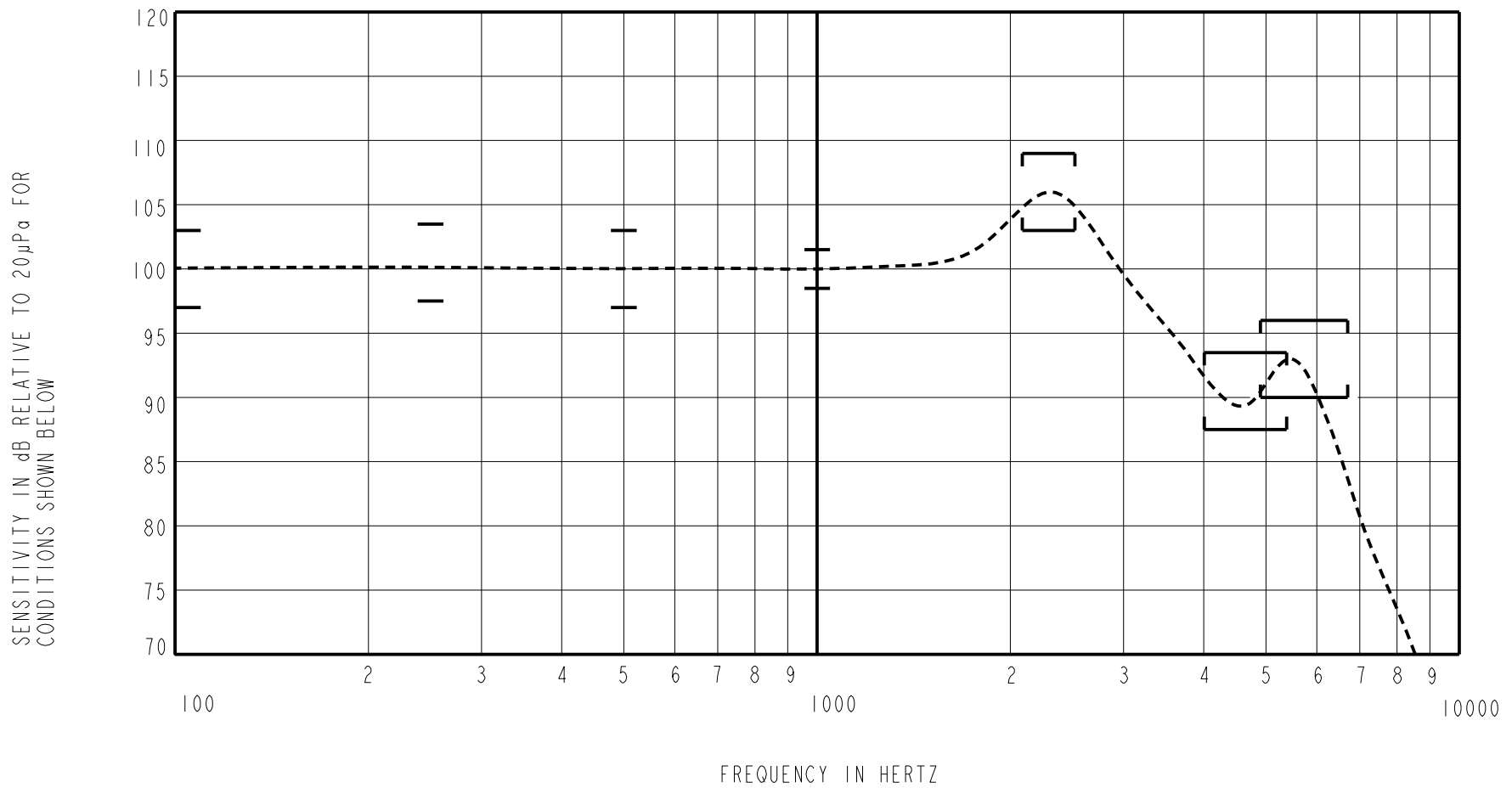
Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
A	C10113826	7-6-12	Active	A
SCALE: 5:1			DR. BY DATE	
DO NOT SCALE DRAWING			LSY 7-6-12	
TITLE: RECEIVER			CK. BY DATE	
OUTLINE DRAWING			GJP 7-13-12	
CD30-31719-000			APP. BY DATE	
SHT 1.1			GJP 7-13-12	

INTENDED FOR USE IN CIC, RIC, AND MINI-BTE APPLICATIONS. THIS IS A PAIR OF GE RECEIVERS WITH VERY LOW VIBRATION IN ALL DIRECTIONS. ONE GE30 RECEIVER IS REVERSE MAGNETIZED FOR MAGNETIC LEAKAGE CONSIDERATIONS. THE GD30 HAS ANNEALED MUMETAL CUPS TO REDUCE MAGNETIC LEAKAGE.

NO DAMPING

CD30-31719-000
SHEET 2.1

CONSTANT VOLTAGE DRIVE CONDITIONS



ACOUSTICAL

SENSITIVITY DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS DESCRIBED IN TABLE 4. NOMINAL SENSITIVITY AT 1kHz IS dB RELATIVE TO 20µPa. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT 1kHz.

LIMIT TYPE	FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
REL	100	-3.0	0.0	+3.0
REL	250	-2.5	+0.5	+3.5
REL	500	-3.0	0.0	+3.0
REF	1000	-1.5	100.0	+1.5
PEAK	2070 - 2530	+3.0	+6.0	+9.0
VALLEY	4000 - 5400	-13.5	-10.5	-7.5
PEAK	4900 - 6700	-10.0	-7.0	-4.0

TABLE 1

TOTAL HARMONIC DISTORTION DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (Vrms)	DC BIAS (V)	LIMIT (%)
833	0.289	0	3
1250	0.289	0	3
833	0.813	0	8
1250	0.813	0	8

TABLE 2

MAXIMUM OUTPUT LEVEL (TYPICAL)

POWER (mW)	500 Hz SPL (dB)	REQUIRED VOLTAGE (Vrms)	Peak SPL (dB)	REQUIRED VOLTAGE (Vrms)
10	114.1	1.5	123.8	2.2

TABLE 3

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.289 Vrms, 0 mA DC BIAS
SOURCE IMPEDANCE	<1 Ohm
TUBING	10 mm [.394"] LONG X 1 mm [.039"] I.D. ("ITE")
COUPLER CAVITY	2 CM ³ , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 60318-5)
MAXIMUM DRIVE VOLTAGE	2.68 Vrms

TABLE 4

ELECTRICAL

DC RESISTANCE @ 20°C	201 Ohms ± 10%
IMPEDANCE @ 500 Hz	228 Ohms ± 15%
IMPEDANCE @ 1 kHz	279 Ohms ± 15%
INDUCTANCE @ 500 Hz	22.2 mH TYPICAL
CAPACITANCE @ 10 MHz	4.6 pF TYPICAL

TABLE 5

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

MECHANICAL

PORT LOCATION: 12N

SOLDER TYPE: SAC305

TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE THAN +1/-3 dB AT 500 Hz FROM -17°C TO 63°C

STORAGE: -40°C TO 63°C

SHOCK RESISTANCE: 90% SURVIVAL RATE WITH THD @ 1/3 PEAK FREQUENCY LESS THAN 10%, THD @ 1/2 PEAK FREQUENCY LESS THAN 20% AND LESS THAN 3dB CHANGE IN SENSITIVITY AT 1kHz WHEN SUBJECTED TO 15,000 G.

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Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
A	CI0113826	7-6-12	Active	A

WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION		DR. BY	DATE
TITLE: RECEIVER PERFORMANCE SPECIFICATION		LSY	7-6-12
		GJP	7-13-12
CD30-31719-000 SHT 2.1		APP. BY	DATE
		GJP	7-13-12